# USB Current-Limited Switches with Fault Blanking 

## General Description

The MAX1693/MAX1694 are current-limited, $60 \mathrm{~m} \Omega$ switches with built-in fault blanking. Their accurate preset current limit of 0.7A to 1.0A makes them ideally suited for USB applications. Their low quiescent supply current $(14 \mu \mathrm{~A})$ and shutdown current $(1 \mu \mathrm{~A})$ conserve battery power in portable applications. The MAX1693/ MAX1694 operate with inputs from +2.7 V to +5.5 V , making them ideal for both +3 V and +5 V systems.
A fault signal notifies the microprocessor that the internal current limit has been reached. A 10 ms fault-blanking feature allows momentary faults (such as those caused when hot-swapping into a capacitive load) to be ignored, thus preventing false alarms to the host system. This fault blanking also prevents a fault signal from being issued when the device is powering up.
In the MAX1693, an output overcurrent condition causes the switch to current limit at 0.7A to 1.0A and FAULT to go low after the 10 ms blanking period. When the overcurrent condition is removed, FAULT returns to its high-impedance state. In the MAX1694, any overcurrent longer than 10 ms will latch the switch open and set $\overline{F A U L T}$ low. The latch is cleared by cycling the $\overline{\mathrm{ON}}$ input or by powering up the device again. This feature saves power by preventing the device from thermally cycling on and off in case of a persistent short-circuit condition.
The MAX1693/MAX1694 have several safety features to ensure that the USB port is protected. Built-in thermaloverload protection limits power dissipation and junction temperatures. Both devices have accurate internal current-limiting circuitry to protect the input supply against overload. They are available in space-saving 10-pin $\mu \mathrm{MAX}$ packages.

Pin Configuration


Accurate Current Limit (0.7A min, 1.0A max)
Guaranteed 0.75A Short-Circuit Protection
10ms Internal Fault-Blanking Timeout
No Fault Signal During Power-Up
Latched FAULT Output Turns Off Power Switch
(MAX1694)

- Thermal Shutdown Protection
- +2.7V to +5.5V Supply Range
- $14 \mu \mathrm{~A}$ Supply Current
- Small 10-Pin $\mu$ MAX Package
- UL Approved-\#E211935

Applications
USB Ports and Hubs
Notebook Computers
Portable Equipment
Docking Stations
Hot Plug-In Power Supplies
Battery-Charger Circuits
Ordering Information

| PART | TEMP. RANGE | PIN-PACKAGE |
| :---: | :--- | :--- |
| MAX1693EUB | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | $10 \mu \mathrm{MAX}$ |
| MAX1694EUB | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | $10 \mu \mathrm{MAX}$ |

Typical Operating Circuit


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## ABSOLUTE MAXIMUM RATINGS

IN, $\overline{O N}, \overline{F A U L T}$ to GND. . -0.3 V to +6 V
OUT to GND .......................................................3V to (VIN +0.3 V )
Maximum Continuous Switch Current .....1.2A (internally limited)
OUT Short Circuit to GND OUT Short Circuit to GND
Continuous Power Dissipation ( $\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}$ )

$$
10 \text {-Pin } \mu \mathrm{MAX} \text { (derate } 5.6 \mathrm{~mW} /{ }^{\circ} \mathrm{C} \text { above }+70^{\circ} \mathrm{C} \text { ) ............ } 444 \mathrm{~mW}
$$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

( $\mathrm{V}_{I N}=+5 \mathrm{~V}, \mathbf{T}_{\mathbf{A}}=\mathbf{0}^{\circ} \mathbf{C}$ to $+\mathbf{8 5} \mathbf{5}^{\circ} \mathbf{C}$, unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.)

| PARAMETER | SYMBOL | CONDITIONS |  | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Operating Voltage | VIN |  |  | 2.7 |  | 5.5 | V |
| Quiescent Current | IQ | $V \overline{O N}=$ GND, IOUT $=0$ | Timer not running |  | 14 | 25 | $\mu \mathrm{A}$ |
|  |  |  | Timer running |  | 35 |  |  |
| Off-Supply Current |  | $\mathrm{V} \overline{\mathrm{ON}}=\mathrm{V}$ IN $=$ V OUT $=5.5 \mathrm{~V}$ |  |  | 0.001 | 1 | $\mu \mathrm{A}$ |
| Undervoltage Lockout | UVLO | Rising edge, 100mV hysteresis |  | 2.0 |  | 2.6 | V |
| Off-Switch Leakage |  | $\begin{aligned} & V \overline{O N}=V I N, \\ & V I N=5.5 V, \\ & V_{O U T}=G N D \end{aligned}$ | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ |  | 0.01 | 2 | $\mu \mathrm{A}$ |
|  |  |  | $\mathrm{T}_{\mathrm{A}}=0^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |  |  | 15 |  |
| On-Resistance | Ron | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | $\mathrm{V}_{\text {IN }}=4.4 \mathrm{~V}$ to 5.5 V |  | 60 | 90 | $\mathrm{m} \Omega$ |
|  |  | $\mathrm{T}_{\mathrm{A}}=0^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | $\mathrm{V}_{\text {IN }}=4.4 \mathrm{~V}$ to 5.5 V |  |  | 125 |  |
|  |  |  | V IN $=3 \mathrm{~V}$ |  | 72 | 150 |  |
| Current Limit | ILIMIT | (Note 1) |  | 700 | 850 | 1000 | mA |
| Continuous Short-Circuit Current Limit | ISC | OUT shorted to GND, MAX1693 only (Note 2) |  |  | 500 | 700 | mA |
| $\overline{\mathrm{ON}}$ Input Logic Low Voltage | VIL | V IN $=2.7 \mathrm{~V}$ to 5.5 V |  |  |  | 0.8 | V |
| $\overline{\text { ON }}$ Input Logic High Voltage | VIH | V IN $=2.7 \mathrm{~V}$ to 3.6 V |  | 2 |  |  | V |
|  |  | V IN $=3.7 \mathrm{~V}$ to 5.5 V |  | 2.4 |  |  |  |
| $\overline{\mathrm{ON}}$ Input Leakage |  | $\mathrm{V} \overline{\mathrm{ON}}=\mathrm{VIN}$ or GND |  |  |  | $\pm 1$ | $\mu \mathrm{A}$ |
| FAULT Output Logic Low Voltage | VoL | $\mathrm{ISINK}=1 \mathrm{~mA}, \mathrm{~V} / \mathrm{N}=3 \mathrm{~V}$ |  |  |  | 0.4 | V |
| FAULT Output High Leakage Current |  | $\mathrm{V}_{\mathrm{IN}}=\mathrm{V}_{\overline{\mathrm{FAULT}}}=5.5 \mathrm{~V}$ |  |  |  | 1 | $\mu \mathrm{A}$ |
| Fault-Blanking Timeout Period | tFB | From overcurrent condition to $\overline{\mathrm{FAULT}}$ assertion |  | 7 | 10 | 13 | ms |
| Start-Up Time |  | $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}, \text { COUT }=150 \mu \mathrm{~F}, \mathrm{R}_{\mathrm{L}}=15 \Omega,$ from $\overline{\mathrm{ON}}$ driven low to $50 \%$ full VOUT |  |  | 1 |  | ms |
| Switch Turn-On Time | ton | ILOAD $=400 \mathrm{~mA}$ |  |  | 80 | 200 | $\mu \mathrm{s}$ |
| Switch Turn-Off Time | tOFF | ILOAD $=400 \mathrm{~mA}$ |  | 3 | 6 | 20 | $\mu \mathrm{s}$ |
| Thermal Shutdown Threshold |  |  |  |  | 165 |  | ${ }^{\circ} \mathrm{C}$ |

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## ELECTRICAL CHARACTERISTICS

(VIN $=+5 \mathrm{~V}, \mathbf{T}_{\mathbf{A}}=\mathbf{- 4 0 ^ { \circ }} \mathbf{C}$ to $+\mathbf{8 5}{ }^{\circ} \mathrm{C}$, unless otherwise noted.) (Note 3)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Operating Voltage | VIN |  | 3 | 5.5 | V |
| Quiescent Current | IQ | $\mathrm{V} \overline{\mathrm{ON}}=\mathrm{GND}$, IOUT $=0$, timer not running |  | 25 | $\mu \mathrm{A}$ |
| Off-Supply Current |  | $\mathrm{V} \overline{\mathrm{ON}}=\mathrm{VIN}=$ VOUT $=5.5 \mathrm{~V}$ |  | 2 | $\mu \mathrm{A}$ |
| Undervoltage Lockout | UVLO | Rising edge, 100mV hysteresis | 2.0 | 2.9 | V |
| Off-Switch Leakage |  | $\mathrm{V} \overline{\mathrm{ON}}=\mathrm{V}$ IN $=5.5 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=\mathrm{GND}$ |  | 15 | $\mu \mathrm{A}$ |
| On-Resistance | Ron | $\mathrm{V}_{\mathrm{I}}=4.4 \mathrm{~V}$ to 5.5 V |  | 125 | $\mathrm{m} \Omega$ |
|  |  | V IN $=3 \mathrm{~V}$ |  | 150 |  |
| Current Limit | ILIMIT | (Note 1) | 640 | 1060 | mA |
| Continuous Short-Circuit Current Limit |  | OUT shorted to GND, MAX1693 only (Note 2) |  | 750 | mA |
| $\overline{\mathrm{ON}}$ Input Logic Low Voltage | VIL | V IN $=3 \mathrm{~V}$ to 5.5 V |  | 0.8 | V |
| $\overline{\text { ON }}$ Input Logic High Voltage | VIH | V IN $=3 \mathrm{~V}$ to 3.6 V | 2 |  | V |
|  |  | V IN $=3.7 \mathrm{~V}$ to 5.5 V | 2.4 |  |  |
| $\overline{\mathrm{ON}}$ Input Leakage |  | V $\overline{O N}=V_{\text {IN }}$ or GND |  | $\pm 1$ | $\mu \mathrm{A}$ |
| FAULT Output Logic Low Voltage | Vol | $\mathrm{ISINK}=1 \mathrm{~mA}, \mathrm{~V}$ IN $=3 \mathrm{~V}$ |  | 0.4 | V |
| $\overline{\text { FAULT Output High Leakage }}$ Current |  | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\overline{\mathrm{FAULT}}}=5.5 \mathrm{~V}$ |  | 1 | $\mu \mathrm{A}$ |
| Fault-Blanking Timeout Period | tFB | From overcurrent condition to $\overline{\text { FAULT }}$ assertion | 6 | 14 | ms |
| Switch Turn-On Time | ton | ILOAD $=400 \mathrm{~mA}$ |  | 200 | $\mu \mathrm{s}$ |
| Switch Turn-Off Time | toff | ILOAD $=400 \mathrm{~mA}$ | 1 | 20 | $\mu \mathrm{s}$ |

Note 1: MAX1693's current limit is tested by forcing Vout to 4.5 V . The MAX1694's current limit is tested by increasing the output current until the switch is latched off.
Note 2: This specification applies to the MAX1693 only. The MAX1694 latches the switch off under a sustained (>10ms) shortcircuit condition
Note 3: Specifications to $-40^{\circ} \mathrm{C}$ are guaranteed by design, not production tested.

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$\left(\mathrm{VIN}=+5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right.$, unless otherwise noted. $)$

OFF-SUPPLY CURRENT vs. TEMPERATURE


NORMALIZED OUTPUT CURRENT vs. OUTPUT VOLTAGE


FAULT-BLANKING TIMEOUT
vs. TEMPERATURE


# USB Current-Limited Switches with Fault Blanking 

## Typical Operating Characteristics (continued)

$\left(\mathrm{V} I \mathrm{~N}=+5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right.$, unless otherwise noted. $)$

MAX1693

$\mathrm{CH} 1=\mathrm{V}_{\mathrm{IN}}, 200 \mathrm{mV} / \mathrm{div}, \mathrm{AC}-\mathrm{COUPLED} ; \mathrm{CH} 2=\mathrm{V}_{\text {OUT }}$, $5 \mathrm{~V} / \mathrm{div} ; \mathrm{CH} 3=\mathrm{V}_{\text {FAULT }}, 5 \mathrm{~V} / \mathrm{div} ; \mathrm{CH} 4=$ I Out, $500 \mathrm{~mA} / \mathrm{div}$

MAX1693 SWITCH TURN-ON TIME

$\mathrm{CH1}=\mathrm{V}_{\text {OUt }}, 5 \mathrm{~V} / \mathrm{div} ; \mathrm{CH} 2=\mathrm{V}_{\text {Ō, }}, 5 \mathrm{~V} / \mathrm{div} ;$ $\mathrm{CH} 3=\mathrm{V}_{\text {FAULT }}, 5 \mathrm{~V} /$ div, CH4 $=1 \mathrm{lout}, 200 \mathrm{~mA} /$ div

MAX1694 OVERCURRENT TO FAULT AND SWITCH LATCHED OFF

$\mathrm{CH} 1=\mathrm{V}_{\text {FAULT }}, 5 \mathrm{~V} / \mathrm{div} ; \mathrm{CH} 2=\mathrm{I}_{\text {OUT }}, 5 \mathrm{~V} / \mathrm{div}$;
$\mathrm{CH} 3=\mathrm{I}_{\text {Out }}, 1 \mathrm{~A} / \mathrm{div}$

MAX1693
CURRENT-LIMIT RESPONSE


CH1 $=\mathrm{V}_{\mathrm{IN}}, 200 \mathrm{mV} / \mathrm{div}$, AC-COUPLED; CH2 $=\mathrm{V}_{\text {OUT }}$, $5 V /$ div; $\mathrm{CH} 3=\mathrm{V}_{\text {FAULT }}$, $5 \mathrm{~V} /$ div; $\mathrm{CH} 4=1$ Iout, $1 \mathrm{~A} /$ div

MAX1693 SWITCH TURN-OFF TIME

$\mathrm{CH} 1=\mathrm{V}_{\text {Out }}, 5 \mathrm{~V} / \mathrm{div} ; \mathrm{CH} 2=\mathrm{V} \overline{\mathrm{ON}}, 5 \mathrm{~V} / \mathrm{div} ;$
$\mathrm{CH} 3=\mathrm{V}_{\text {FAULT }}, 5 \mathrm{~V} / \mathrm{div} ; \mathrm{CH} 4=\mathrm{I}_{\text {OUt }}, 200 \mathrm{~mA} /$ div

## MAX1693

START-UP TIME (TYPICAL USB APPLICATION)


CH1 $=\mathrm{V}_{\text {OUt }}$, $5 \mathrm{~V} / \mathrm{div}$; CH2 $=\mathrm{V}_{\text {ON }}$, $5 \mathrm{~V} /$ div;
CH3 $=I_{\text {OUT }}, 500 \mathrm{~mA} /$ div; $C H 4=V_{\text {FAULT }}, 5 V /$ div

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| PIN | NAME | FUNCTION |
| :---: | :---: | :--- |
| $1,3,9$ | IN | Input. P-channel MOSFET source. Connect all IN pins together and bypass with a $1 \mu F$ <br> ceramic capacitor to ground. |
| $2,4,8,10$ | $\overline{\text { ONT }}$ | Switch Output. P-channel MOSFET drain. Connect all OUT pins together and bypass with a <br> $0.1 \mu F$ capacitor to ground. |
| 5 | GND | Active-Low Switch On Input. A logic low turns the switch on. |
| 6 | $\overline{\text { FAULT }}$ | Fault-Indicator Output. This open-drain output goes low when the device is in thermal shut- <br> down or undervoltage lockout or on a sustained $(>10 m s) ~ c u r r e n t-l i m i t ~ c o n d i t i o n . ~ F o r ~ t h e ~$ <br> MAX1694 only, this output latches low when asserted, and the power switch is turned off <br> until the latch is reset. |
| 7 |  |  |



Figure 1. MAX1693 Functional Diagram

## Detailed Description

The MAX1693/MAX1694 P-channel MOSFET power switches limit output current to 0.7 A min to 1.0 A max. When the output current is increased beyond the current limit (ILIMIT), the current also increases through the replica switch (lout/6500). The current-limit error amplifier compares the voltage to the internal 1.24V reference and regulates the current back to the ILIMIT.

These switches are not bidirectional; therefore, the input voltage must be higher than the output voltage.

Continuous Short-Circuit Protection
The MAX1693/MAX1694 are foldback short circuit-protected switches. In the event of an output short-circuit or current-overload condition, the current through the switch is foldback-current limited to 500 mA continuous for the MAX1693. For the MAX1694, if the short circuit lasts longer than 10 ms , the switch is latched off.

## Thermal Shutdown

The MAX1693/MAX1694 feature thermal shutdown. The switch turns off and the $\overline{\text { FAULT }}$ output goes low immediately (no fault blanking) when the junction temperature exceeds $+165^{\circ} \mathrm{C}$. The MAX1694 remains latched with the switch off and the FAULT output low. When the MAX1693 cools $20^{\circ} \mathrm{C}$, the switch turns back on. If the fault short-circuit condition is not removed, the switch will cycle on and off, resulting in a pulsed output.

## FAULT Indicator

The MAX1693/MAX1694 provide a fault output ( $\overline{\mathrm{FAULT}})$. A $100 \mathrm{k} \Omega$ pull-up resistor from $\overline{\mathrm{FAULT}}$ to IN provides a logic control signal. This open-drain output goes low when any of the following conditions occur:

- The input voltage is below the undervoltage-lockout (UVLO) threshold.
- The die temperature exceeds the thermal shutdown temperature limit of $+165^{\circ} \mathrm{C}$.
- The device is in current limit and the 10 ms faultblanking period is exceeded.

Fault Blanking
The MAX1693/MAX1694 feature 10ms fault blanking. Fault blanking allows current-limit faults, including

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momentary short-circuit faults that occur when hotswapping a capacitive load, and also ensures that no fault is issued during power-up. When a load transient causes the device to enter current limit, an internal counter starts. If the load fault persists beyond the 10ms fault-blanking timeout, the FAULT output asserts low. Ensure that the MAX1693/MAX1694's input is adequately bypassed to prevent input glitches from triggering spurious FAULT outputs. Input voltage glitches less than 150 mV will not cause a spurious FAULT output. Load-transient faults less than 10 ms (typ) will not cause a $\overline{\text { FAULT }}$ output assertion.
Only current-limit faults are blanked. Die overtemperature faults and input voltage droops below the UVLO threshold will cause an immediate fault output.

Fault Latching (MAX1694 Only)
The MAX1694 features a latched FAULT output. Whenever the FAULT output is activated, it latches the FAULT output low and also turns the switch off. To clear the latch, either cycle the $\overline{\mathrm{ON}}$ input or cycle the input voltage below UVLO.

## Applications Information

## Input Capacitor

To limit the input voltage drop during momentary output short-circuit conditions, connect a capacitor from IN to GND. A $1 \mu \mathrm{~F}$ ceramic capacitor will be adequate for most applications; however, higher capacitor values will further reduce the voltage drop at the input. See Figure 2.

## Output Capacitor

Connect a $0.1 \mu \mathrm{~F}$ capacitor from OUT to GND. This capacitor helps prevent inductive parasitics from pulling OUT negative during turn-off.

*USB SPECIFICATIONS REQUIRE A LARGER CAPACITOR
Figure 2. Typical Application Circuit

## Layout and Thermal Dissipation

To optimize the switch-response time to output shortcircuit conditions, it is very important to keep all traces as short as possible to reduce the effect of undesirable parasitic inductance. Place input and output capacitors as close to the device as possible (no more than 5 mm ). All IN and all OUT pins must be connected with short traces to the power bus. Wide power bus planes will provide superior heat dissipation through the switch IN and OUT pins. Figure 3 shows suggested pin connections for a single-layer board.
Under normal operating conditions, the package can dissipate and channel heat away. Calculate the maximum power dissipation as follows:

$$
P=(\text { ILIMIT })^{2} \cdot \operatorname{RON}
$$

where ILIMIT is the preset current limit (1.0A max) and RON is the on-resistance of the switch ( $125 \mathrm{~m} \Omega$ max).
When the output is short-circuited, foldback-current limiting activates and the voltage drop across the switch equals the input supply. The power dissipated across the switch increases, as does the die temperature. If the fault condition is not removed, the thermal-overload-protection circuitry activates (see the Thermal Shutdown section). Wide power-bus planes connected to IN and OUT and a ground plane in contact with the device will help dissipate additional heat.

## Chip Information

TRANSISTOR COUNT: 715


Figure 3. IN and OUT Cross Connections for a Single-Layer Board

USB Current-Limited Switches with Fault Blanking


Note: Neither the MAX1693 nor MAX1694 has an exposed pad.

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[^0]:    Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

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